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 $\succ$

A diagram of a 10-bit shift register. It consists of a horizontal row of 10 rectangular cells. Above each cell is an input label: A, B, C, D, E, F, G, H, I, J. Below each cell is an output label: A, B, C, D, E, F, G, H, I, J. The cells are connected in a chain, with the output of one cell being the input to the next cell to its right. The first cell (A) has an additional input line from the left.

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- 0000 0000 0000 00 n00
- 0000 0000 0000 00 00
- 0000 0000 0000 00 000000
- 0000 0000 0000 00 00
- 0000 0000 0000 00 00000000
- 0000 0000 0000 00 0000000
- 0000 0000 0000 00 00
- 00000000 00 0000 xxi
- 0000 0000 N0000
- 0000 0000 0000
- 0000 0000 0000
- 0000 0000 0000
- 0000 0000 00 00
- 0000 0000 00
- 0000 0000 00
- 0000 0000 00000
- 0000 0000 00
- 0000 0000 00
- 0000 000000 0000 a00
- 0000 000000 0000 0
- 0000 000000 0000 00
- 0000 000000 0000 000000
- 0000 000000 0000 00 00
- 0000 0000000000
- 0000 00 00000000
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2021-04-19

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2021-04-14

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Email:Tbm_rAAO@gmail.com

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